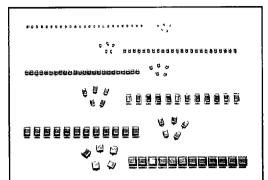
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# TYPE 595D Solid Tantalum Chip Capacitors







### **FEATURES**

- Compatible with 293D molded chip mounting pad layout
- · Smallest size/rating in the industry
- · Largest capacitance rating in the industry
- Terminations: 60/40 Tin Lead (2) standard
- 8mm, 12mm tape available per EIA-481-1 and reeling per IEC 286-3. 7" [178mm] standard. 13" [330mm] available.
- Operating temperature: 55°C to + 125°C
- Case code compatibility with EIA 535BAAC and CECC 30801 molded chips
- · Low leakage current, impedance and inductance

### PERFORMANCE CHARACTERISTICS

**Operating Temperature:** - 55°C to + 85°C. (To + 125°C with voltage derating.)

**Capacitance Tolerance:** At 120Hz, + 25°C. ± 20% standard. ± 10% available.

Dissipation Factor: At 120Hz, + 25°C. Dissipation factor, as determined from the expression 2πfRC, shall not exceed the values listed in the Standard Ratings Tables.

DC Leakage Current (DCL Max.):

At + 25°C: Leakage current shall not exceed the values listed in the Standard Ratings Tables.

At + 85°C: Leakage current shall not exceed 10 times the values listed in the Standard Ratings Tables.

**At + 125°C:** Leakage current shall not exceed 12 times the values listed in the Standard Ratings Tables.

**Life Test:** Capacitors shall withstand rated DC voltage applied at + 85°C for 2000 hours or derated DC voltage applied at + 125°C for 1000 hours.

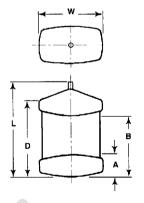
Following the life test:

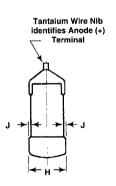
- 1. DCL shall meet the original requirement.
- 2. Dissipation Factor and ESR shall meet the initial requirement.
- 3. Change in capacitance shall not exceed  $\pm$  10%.

### **ELECTRICAL CHARACTERISTICS @ + 25°C**

**DCL:** .01 $\mu$ A/CV or .5 $\mu$ A whichever is greater. **DF:**  $4\% \le 1\mu$ F.  $6\% > 1\mu$ F <  $100\mu$ F.  $8\% \ge 100\mu$ F.

### **DIMENSIONAL CONFIGURATIONS** [Numbers in brackets indicate millimeters]





CASE CODE	L (Max.)	W	н	A	В	D (Ref.)	J (Max.)
Т	087	.045 ± .010	.045 ± .010	.016 ± .008	.042 ± .010	.063	.004
	[2 2]	[1 1 ± 0.3]	[1 1 ± 0.3]	[.4 ± 0.2]	[1.07 ± 0.25]	[1.6]	[0.1]
Α	.146	.072 ± .010	.056 ± .012	.023 ± .010	.085 ± .015	.115	.004
	[3 7]	[1.8 ± 0.3]	[1.4 ± 0.3]	[.6 ± 0.3]	[2.2 ± 0.4]	[2.9]	[0.1]
В	.158	.104 ± .010	.062 ± .012	.023 ± .010	.097 ± .015	.127	.004
	[4.0]	[2.6 ± 0.3]	[1.6 ± 0.3]	[.6 ± 0.3]	[2 5 ± 0.4]	[3.2]	[0.1]
С	.281	.126 ± .010	.098 ± .012	.040 ± .015	.200 ± .025	.236	.004
	[7.1]	[3.2 ± 0.3]	[2.5 ± 0.3]	[1.0 ± 0.4]	[5.1 ± 0.6]	[6.0]	[0.1]
D	.293	.170 ± .010	.110 ± .012	.050 ± .015	.200 ± 025	.253	.004
	[7.5]	[4.3 ± 0.3]	[2 8 ± 0 3]	[1.3 ± 0.4]	[5.1 ± 0.6]	[6.4]	[0.1]
R	.283	.235 ± .010	.136 ± .012	050 ± .015	.190 ± .025	.243	.004
	[7.2]	[6.0 ± 0.3]	[3.5 ± 0.3]	[1.3 ± 0.4]	[4.8 ± 0.6]	[6.2]	[0.1]

Note: The anode termination (D less B) will be a minimum of .010" [0.3]. T Case = .005" [.13] minimum.

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	RATED VOLTAGE UR @ + 85°C									
μ <b>F</b>	4 V	6.3 V	10 V	16 V	20 V	25 V	35 V	50 V		
0.10	i							Т		
0.15								Т		
0.22								Т		
0.33							Т	Α		
0.47						Т	Α	Α		
0.68					Т		Α	В		
1.0		·			T		Α	В		
1.5				T		Α	В	С		
2.2			Т		Α		В	С		
3.3		Т			Α	В	С	С		
4.7	Т			Α	В		С	С		
6.8				A	В		С	D		
10			Α	В		С	D	R		
15	Α	Α	В	В		С	D	R		
22		В			С	D	R			
33	В			С		D	R			
47				С	D	R				
68			С	D		R				
100		С		D	R					
120	С	D	D	R	R					
150	С		D	R						
180	D	D	R	R						
220	D	D	R							
270	D		R	"						
330		R	R							
390	R	R								
	R	<del>                                     </del>								

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CAPACITANCE (μF)	CASE CODE	PART NUMBER*	Max. DCL @ + 25°C (μA)	Max. DF @ + 25°C 120Hz (%)	Max. ESR @ + 25°C 100kHz (Ohms)	Max. RIPPLI 100kHz Irms (Amps)
	4 WVDC	C @ + 85°C, SURGE = 5.2	V 2.7 WVDC @	@ + 125°C, SURG	iE = 3.4 V	
4.7	Т	595D475X0004T2T	0.5	6	8.7	0.08
15.0	Α	595D156X0004A2T	0.6	6	3.5	0.14
33.0	В	595D336X0004B2T	1.3	6	2.0	0.20
120.0	С	595D127X0004C2T	4.8	8	0.65	0.47
150.0	C	595D157X0004C2T	6.0	8	0.70	0.40
180.0	D	595D187X0004D2T	7.2	8	0.55	0.58
220.0	D	595D227X0004D2T	8.8	8	0.45	0.58
270.0	D	595D277X0004D2T	10.8	8	0.40	0.61
390.0	R	595D397X0004R2T	15.6	8	0.16	1.25
470.0	R	595D477X0004R2T	18.8	8	0.15	1.29
	6.3 W	/VDC @ + 85°C, SURGE = 8	8 V 4 WVDC €	⊉ + 125°C, SURG	iE = 5 V	
3.3	Т	595D335X06R3T2T	0.5	6	9.4	0.08
15.0	A	595D156X06R3A2T	0.9	6	3.0	0.15
22.0	В	595D226X06R3B2T	1.4	6	2.2	0.19
100.0	Č	595D107X06R3C2T	6.3	8	0.7	0.40
120.0	Ċ	595D127X06R3C2T	7.6	8	0.65	0.47
180.0	Ď	595D187X06R3D2T	11.3	8	0.55	0.58
220.0	Ď	595D227X06R3D2T	13.9	8	0.35	0.65
330.0	Ä	595D337X06R3R2T	20.8	8	0.18	1.18
390.0	R	595D397X06R3R2T	24.6	8	0.16	1.25
	10 WV	/DC @ + 85°C, SURGE = 1	3 V 7 WVDC €	● + 125°C, SURG	iE = 8 V	
2.2	Т	595D225X0010T2T	0.5	6	9.5	0.08
10.0	A	595D106X0010A2T	1.0	6	3.2	0.15
15.0	В	595D156X0010B2T	1.5	6	2.2	0.19
68.0	Ċ	595D686X0010C2T	6.8	6	0.70	0.40
120.0	Ď	595D127X0010D2T	12.0	8	0.50	0.61
150.0	D	595D157X0010D2T	15.0	8	0.40	0.61

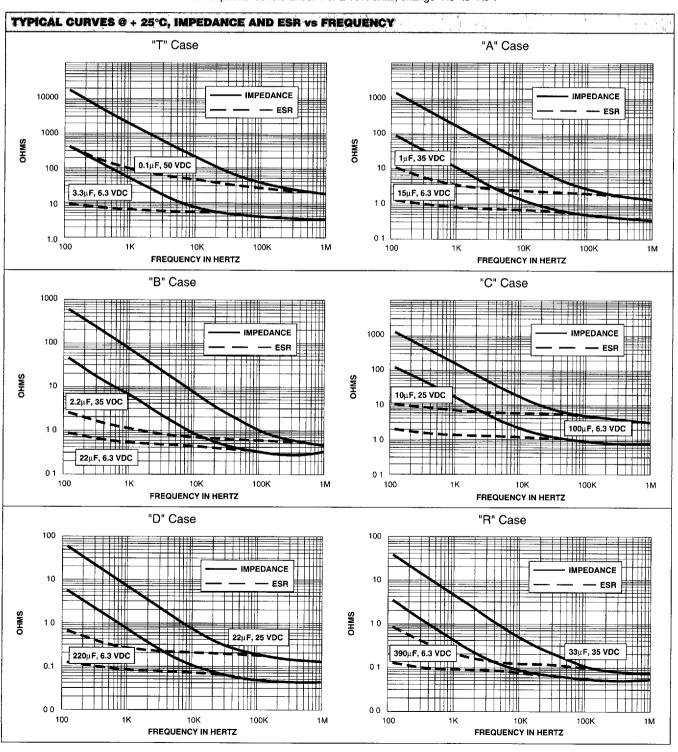
<sup>\*</sup> Part Numbers shown for units with  $\pm$  20% capacitance tolerance. For  $\pm$  10% units, change "X0" to "X9".

CAPACITANCE (μF)	CASE CODE	PART NUMBER*	Max. DCL @ + 25°C (μA)	Max. DF @ + 25°C 120Hz (%)	Max. ESR @ + 25°C 100kHz (Ohms)	Max. RIPPLE 100kHz Irms (Amps)
(4.7)		/DC @ + 85°C, SURGE =			iE = 8 V	
100.0		595D187X0010R2T	18 0	8	0 22	1 05
180.0	R R	595D227X0010R2T	22 0	8	0.20	1.12
220 0			27 0	8	0.20	1.17
270.0	R	595D277X0010R2T	33 0	8	0.15	1 29
330.0	R	595D337X0010R2T				1 29
	16 WVI	OC @ + 85°C, SURGE = 2	0 V 10 WVDC	@ + 125°C, SURG	iE = 12 V	
1.5	Т	595D155X0016T2T	0.5	6	9.7	0.675
4.7	Α	595D475X0016A2T	0.8	6	3.6	0.14
6.8	Α	595D685X0016A2T	1 1	6	3 2	0.15
10.0	В	595D106X0016B2T	1.6	6	2.2	0.19
15 0	В	595D156X0016B2T	2.4	6	1.8	0.21
33.0	С	595D336X0016C2T	5.3	6	09	0.35
47.0	С	595D476X0016C2T	7.5	6	0.7	0.40
68.0	D	595D686X0016D2T	10.9	6	0.50	0.55
100.0	D	595D107X0016D2T	16.0	8	0.45	0.58
120.0	Ř	595D127X0016R2T	19.2	8	0 26	0.98
	R	595D157X0016R2T	24.0	8	0 22	1.07
150.0			28.8	8	0.22	1.07
180.0	R	595D187X0016R2T				1.07
	20 WVI	DC @ + 85°C, SURGE = 2	6 V 13 WVDC	@ + 125°C, SUR0	E = 16 V	
0.68	Т	595D684X0020T2T	0.5	4	12.0	0.065
1.0	Т	595D105X0020T2T	0.5	4	10.0	0.07
2.2	Å	595D225X0020A2T	0.5	6	4.5	0.13
3.3	Â	595D335X0020A2T	0.7	6	3.8	0.14
	B	595D475X0020B2T	0.9	6	2.8	0.17
4.7			1.4	6	2.4	0.18
6.8	В	595D685X0020B2T		6	0.9	0.35
22.0	C	595D226X0020C2T	4.4			
47.0	D	595D476X0020D2T	9.4	6	0.50	0.55
100.0	R	595D107X0020R2T	20.0	8	0.25	1.00
120.0	R	595D127X0020R2T	24.0	8	0.25	1.00
	25 WV	DC @ + 85°C, SURGE = 3	2 V 17 WVDC	@ + 125°C, SURC	GE = 20 V	
0.47	Т	595D474X0025T2T	0.5	4	15.0	0.06
1.5	Α	595D155X0025A2T	0.5	6	5.0	0.12
3.3	В	595D335X0025B2T	0.8	6	2.8	0.17
10.0	С	595D106X0025C2T	25	6	1.2	0.20
15.0	Ċ	595D156X0025C2T	3.8	6	1.0	0 30
22 0	D	595D226X0025D2T	5.5	6	0.80	0.36
	D		8.3	6	0.60	0.43
33.0		595D336X0025D2T		6	0.27	0.96
47.0 68.0	R R	595D476X0025R2T 595D686X0025R2T	11.8 17.0	6	0.25	1.00
00.0		DC @ + 85°C, SURGE = 4				
					16 0	0.055
0.33	T A	595D334X0035T2T 595D474X0035A2T	0.5 0.5	4 4	7.0	0.000
0.47	A		0.5	4	6.0	0.11
0.68	A	595D684X0035A2T		7 1	5.5	0.11
1.0	A	595D105X0035A2T	0.5	4	4.2	0.14
1.5	В	595D155X0035B2T	05	6		
2.2	В	595D225X0035B2T	08	6	35	0.15
3.3	C	595D335X0035C2T	1.2	6	20	0.24
4.7	C	595D475X0035C2T	1.6	6	1.5	0.27
6.8	С	595D685X0035C2T	2.4	6	1.3	0.29
10.0	D	595D106X0035D2T	35	6	0.9	0.41
15.0	D	595D156X0035D2T	5.3	6	0.8	0.43
22.0	R	595D226X0035R2T	7.7	6	0.6	0 65
33.0	R	595D336X0035R2T	11.6	6	0.5	0.71
	50 WV	DC @ + 85°C, SURGE = 6	55 V 33 WVDC	@ + 125°C, SUR	GE = 38 V	
0 10	Т	595D104X0050T2T	0.5	4	25.0	0.035
0.15	Ť	595D154X0050T2T	0.5	4	20.0	0.038
0.22	Ť	595D224X0050T2T	0.5	4	17.0	0.04
	Ä	595D224X0050121 595D334X0050A2T	0.5	4	9.0	0.09
0.33	A	595D334X0050A2T 595D474X0050A2T	0.5	, <u>4</u>	8.0	0.09
0 47			0.5	т И	6.0	0.12
0 68	В	595D684X0050B2T		<del>т</del> Л	5.0	0.12
10	В	595D105X0050B2T	0.5	7		
1.5	С	595D155X0050C2T	0.8	6	3.0	0.19

<sup>\*</sup> Part Numbers shown for units with  $\pm$  20% capacitance tolerance. For  $\pm$  10% units, change "X0" to "X9".

STANDARD RATIN	GS		A STATE OF THE STA	31		Charles Charles
CAPACITANCE (μF)	CASE CODE	PART NUMBER*	Max. DCL @ + 25°C (μA)	Max. DF @ + 25°C 120Hz (%)	Max. ESR @ + 25°C 100kHz (Ohms)	Max. RIPPLE 100kHz Irms (Amps)
	50 WVI	OC @ + 85°C, SURGE = 65	V 33 WVDC	<b>@</b> + 125°C, SURG	iE = 38 V	
3.3	С	595D335X0050C2T	1.7	6	2.0	0.23
4.7	С	595D475X0050C2T	2.4	6	1.5	0.27
6.8	D	595D685X0050D2T	3.4	6	1.2	0.35
10	R	595D106X0050R2T	5.0	6	0.90	0.53
15	R	595D156X0050R2T	7.5	6	0.80	0.56

<sup>\*</sup> Part Numbers shown for units with  $\pm$  20% capacitance tolerance. For  $\pm$  10% units, change "X0" to "X9".



### PERFORMANCE CHARACTERISTICS

- Operating Temperature: Capacitors are designed to operate over the temperature range of - 55°C to + 85°C.
- 1.1 Capacitors may be operated to + 125°C with voltage derating to two-thirds the + 85°C rating.

+ 85°C	Rating	+ 125°C Rating		
Working Voltage (V)	Voltage Voltage		Surge Voltage (V)	
4.0	5.2	2.7	3.4	
6.3	8.0	4.0	5.0	
10.0	13.0	7.0	8 0	
16.0	20.0	10.0	12.0	
20.0	26.0	13.0	16.0	
25 0	32.0	17.0	20.0	
35.0	46.0	23.0	26.0	
50.0	65.0	33.0	38.0	

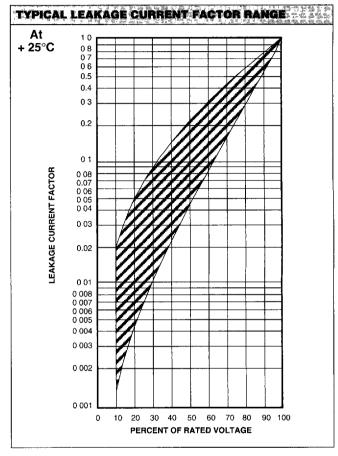
- DC Working Voltage: The DC working voltage is the maximum operating voltage for continuous duty at the rated temperature.
- 3. **Surge Voltage:** The surge DC rating is the maximum voltage to which the capacitors may be subjected under any conditions, including transients and peak ripple at the highest line voltage.
- 3.1 Surge Voltage Test: Capacitors shall withstand the surge voltage applied in series with a 33 ohm ± 5% resistor at the rate of one-half minute on, one-half minute off, at + 85°C, for 1000 successive test cycles.
- 3.2 Following the surge voltage test, the dissipation factor and the leakage current shall meet the initial requirements; the capacitance shall not have changed more than  $\pm$  10%.
- Capacitance Tolerance: The capacitance of all capacitors shall be within the specified tolerance limits of the nominal rating.
- 4.1 Capacitance measurements shall be made by means of a polarized capacitance bridge. The polarizing voltage shall be of such magnitude that there shall be no reversal of polarity due to the AC component. The maximum voltage applied to capacitors during measurements shall be 2 volts rms at 120Hz at + 25°C. If the AC voltage applied is less than one-half volt rms, no DC bias is required. Accuracy of the bridge shall be within ± 2%.
- 5. Capacitance Change With Temperature: The capacitance change with temperature shall not exceed the following percentage of the capacitance measured at + 25°C:

- 55°C	+ 85°C	+ 125°C
- 10%	+ 10%	+ 12%

- Dissipation Factor: The dissipation factor, determined from the expression 2πfRC, shall not exceed values listed in the Standard Ratings Table.
- 6.1 Measurements shall be made by the bridge method at, or referred to, a frequency of 120Hz and a temperature of + 25°C.

7. Leakage Current: Capacitors shall be stabilized at the rated temperature for 30 minutes. Rated voltage shall be applied to capacitors for 5 minutes using a steady source of power (such as a regulated power supply) with a 1000 ohm resistor connected in series with the capacitor under test to limit the charging current. Leakage current shall then be measured.

Note that leakage current varies with applied voltage. See graph below for the appropriate adjustment factor.



- 7.1 At + 25°C, the leakage current shall not exceed the value listed in the Standard Ratings Table.
- 7.2 At + 85°C, the leakage current shall not exceed 10 times the value listed in the Standard Ratings Table.
- 7.3 At + 125°C, the leakage current shall not exceed 12 times the value listed in the Standard Ratings Table.
- Life Test: Capacitors shall withstand rated DC voltage applied at + 85°C for 2000 hours or derated DC voltage applied at + 125°C for 1000 hours.
- 8.1 Following the life test, the dissipation factor and leakage current shall meet the initial requirement; the capacitance change shall not exceed  $\pm$  10% of the initial value.
- 9. **Humidity Test:** Capacitors shall withstand 1000 hours at + 40°C, 90% to 95% relative humidity, with no voltage applied.
- 9.1 Following the humidity test, capacitance change shall be within 10% of initial value; the dissipation factor shall not exceed 150% of the initial requirement; the leakage current shall not exceed 200% of the initial requirement at + 25°C.

### **PERFORMANCE CHARACTERISTICS (Continued)**

- Soldering Testing: Capacitors shall be checked by the following method: Terminations are immersed in non-activated flux and dipped in 60/40 Sn/Pb solder for 5 seconds at + 245°C. Wetting must occur on at least 95% of the external surface of the terminations.
- Resistance to Soldering Heat: Capacitors mounted on a substrate will withstand exposure to + 260°C for 5 seconds.
- 11.1 Following the resistance to soldering heat test, capacitance shall be within initial tolerance;

- dissipation factor shall be within 120% of initial requirements at + 25°C; the leakage current shall be within initial requirement at + 25°C.
- 12. Marking: The small body area of these capacitors does not allow elaborate marking schemes. All required information is present on the carton or package in which the parts are shipped; in addition, part number, quantity and date code are indicated on the reels.

### **GUIDE TO APPLICATION**

1. **A-C Ripple Current:** The maximum allowable ripple current shall be determined from the formula:

$$I_{rms} = \sqrt{\frac{P}{R_{ESR}}}$$

where.

P = Power Dissipation in Watts @ + 25°C as given in the table in Paragraph Number 5 (Power Dissipation).

Resr = The capacitor Equivalent Series Resistance at the specified frequency.

A-C Ripple Voltage: The maximum allowable ripple voltage shall be determined from the formula:

$$V_{rms} = Z \cdot \sqrt{\frac{P}{R_{ESR}}}$$

or, from the formula:

$$V_{rms} = I_{rms} \times Z$$

where,

P = Power Dissipation in Watts @ + 25°C as given in the table in Paragraph Number 5 (Power Dissipation).

R<sub>ESR</sub> = The capacitor Equivalent Series Resistance at the specified frequency.

Z = The capacitor Impedance at the specified frequency.

- 2.1 The sum of the peak AC voltage plus the DC voltage shall not exceed the DC voltage rating of the capacitor.
- 2.2 The sum of the negative peak AC voltage plus the applied DC voltage shall not allow a voltage reversal exceeding 10% of the DC working voltage at + 25°C.
- 3. **Reverse Voltage:** These capacitors are capable of withstanding peak voltages in the reverse direction equal to 10% of the DC rating at + 25°C and 5% of the DC rating at + 85°C.
- 4. Temperature Derating: If these capacitors are to be operated at temperatures above + 25°C, the permissible rms ripple current or voltage shall be calculated using the derating factors as shown:

Temperature	Derating Factor
+ 25°C	1.0
+ 55°C	0.9
+ 85°C	0.8
+ 125°C	0.4

5. Power Dissipation: Power dissipation will be affected by the heat sinking capability of the mounting surface. Non-sinusoidal ripple current may produce heating effects which differ from those shown. It is important that the equivalent *Irms* value be established when calculating permissible operating levels. (Power Dissipation calculated using 25°C temperature rise.)

Case Code	Maximum Permissible Power Dissipation @ + 25°C (Watts) in free air
т	0.030
Α	0.070
В	0.080
С	0.110
D	0.150
R	0.250

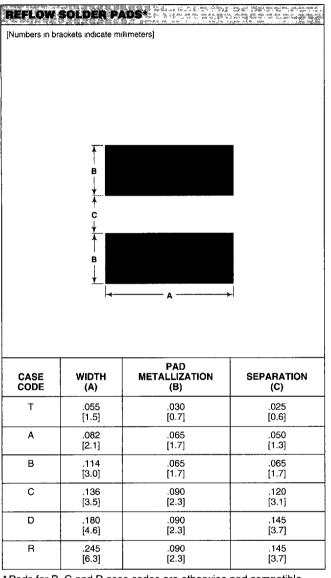
6. **Printed Circuit Board Materials:** The 595D is compatible with most commonly used printed circuit board materials (alumina substrates, FR4, FR5, G10, PTFE-fluorocarbon and porcelainized steel). If your desired board material is not shown here please contact the Tantalum Marketing Department for assistance in determining compatibility.

### 7. Attachment:

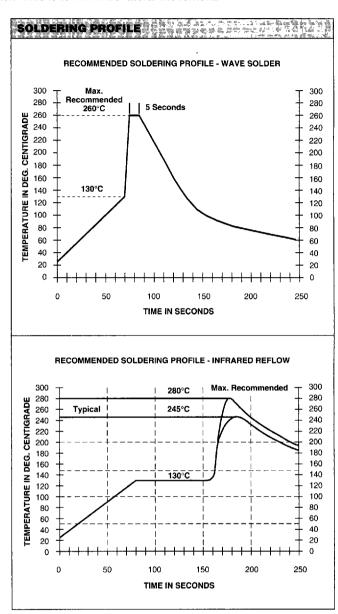
- 7.1 **Solder Paste:** The recommended thickness of the solder paste after application is .007" ± .001" [.178 ± .025]. Care should be exercised in selecting the solder paste. The metal purity should be as high as practical. The flux (in the paste) must be active enough to remove the oxides formed on the metallization prior to the exposure to soldering heat. In practice this can be aided by extending the solder preheat time at temperatures below the liquidous state of the solder.
- 7.2 Soldering: Capacitors can be attached by conventional soldering techniques vapor phase, infrared reflow, wave soldering and hot plate methods. The solder temperature/soldering time chart shows maximum recommended time/temperature conditions for soldering. Attachment with a soldering iron is not recommended due to the difficulty of controlling temperature and time at temperature.
- Cleaning (Flux Removal) After Soldering: The 595D is compatible with all commonly used solvents such as TES, TMS, Prelete and Chlorethane. Solvents containing methylene chloride or other epoxy solvents should be avoided since these will attack the epoxy encapsulation material.

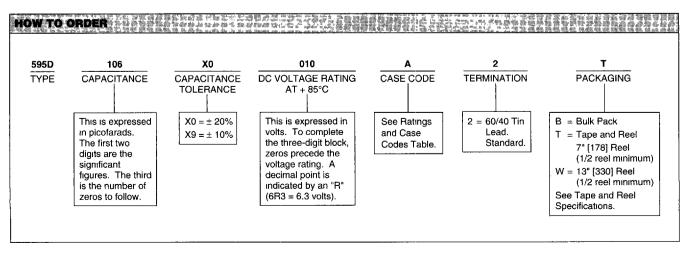
### **GUIDE TO APPLICATION (Continued)**

9. Recommended Mounting Pad Geometries: The area under the tantalum wire nib should not be metallized on the PC board. The nib must have sufficient clearance to avoid electrical contact with other components. The width dimension indicated is the same as the maximum width of the capacitor. This is to minimize lateral movement.



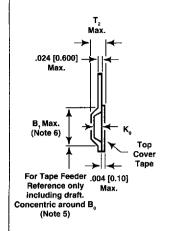
<sup>\*</sup> Pads for B, C and D case codes are otherwise pad compatible with Type 293D, B, C and D case codes respectively.

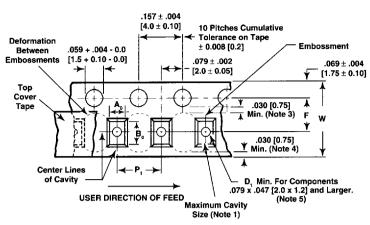


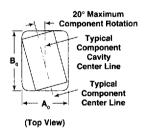


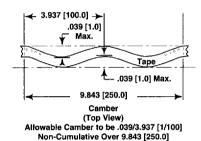
## TAPE AND REEL PACKAGING

Please Note: [Numbers in brackets indicate millimeters] Metric dimensions will govern. Dimensions in inches are rounded and for reference only









Tape and Reel Specifications: All case codes are available on plastic embossed tape per EIA-481-1. Tape reeling per IEC 286-3 is also available. Standard reel diameter is 7" [178]. 13" [330] reels are available and recommended as the most cost effective packaging method.

Maximum

Component

Rotation

(Side or Front Sectional View)

The most efficient packaging quantities are full reel increments on a given reel diameter. The quantities shown allow for the sealed empty pockets required to be in conformance with EIA-481-1. Reel size and packaging orientation must be specified in the Sprague® part number.

TAPE SIZE	B <sub>1</sub> (Max.) (Note 6)	D <sub>1</sub> (Min.) (Note 5)	F	P,	R (Min.) (Note 2)	T <sub>2</sub> (Max.)	w	A <sub>o</sub> B <sub>o</sub> K <sub>o</sub>
8mm	.179 [4.55]	.039 [1.0]	.138 ± .002 [3.5 ± 0.05]	.157 ± .004 [4.0 ± 0.1]	.984 [25.0]	.098 [2.5]	.315 + .012004 [8.0 + 0.3 - 0.1]	
12mm	.323 [8 2]	.059 [1.5]	.217 ± .002 [5.5 ± 0.05]	.157 ± .004 [4.0 ± 0.1]	1.181 [30.0]	.256 [6.5]	.472 ± .012 [12.0 ± .30]	(Note 1)
12mm Double Pitch	.323 [8.2]	.059 [1.5]	.217 ± .002 [5.5 ± 0.05]	.315 ± .004 [8.0 ± 0.1]	1.181 [30.0]	.256 [6.5]	.472 ± .012 [12.0 ± .30]	

### Notes:

- 1. A<sub>0</sub>B<sub>0</sub>K<sub>0</sub> are determined by the maximum dimensions to the ends of the terminals extending from the component body and/or the body dimensions of the component. The clearance between the ends of the terminals or body of the component to the sides and depth of the cavity (A<sub>0</sub>B<sub>0</sub>K<sub>0</sub>) must be within .002" [0.05] minimum and .020" [0.50] maximum. The clearance allowed must also prevent rotation of the component within the cavity of not more than 20 degrees.
- Tape with components shall pass around radius "R" without damage. The minimum trailer length may require additional length to provide R minimum for 12mm embossed tape for reels with hub diameters approaching N minimum.
- This dimension is the flat area from the edge of the sprocket hole to either the outward deformation of the carrier tape between the embossed cavities or to the edge of the cavity whichever is less.
- This dimension is the flat area from the edge of the carrier tape opposite the sprocket holes to either the outward deformation of the carrier tape between the embossed cavity or to the edge of the cavity whichever is less.
- The embossment hole location shall be measured from the sprocket hole controlling the location of the embossment. Dimensions of embossment location and hole location shall be applied independent of each other
- 6. B, dimension is a reference dimension for tape feeder clearance only.

